



# Low-Voltage CMOS RF Frequency Synthesizers

*Howard Cam Luong, Gerry Chi Tak Leung*

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This book provides architectures and design techniques to enable CMOS frequency synthesizers to operate at low-supply voltage at high frequency with good phase noise and low power consumption. It offers in-depth updates on many of these techniques, and introduces useful guidelines and step-by-step procedures on behavior simulations of frequency synthesizers. Finally, the authors feature four successful prototypes to illustrate potential applications of the architectures and design techniques described. Their work will prove useful for engineers, managers, and researchers working in RFIC design for wireless applications.

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